

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Patent Application of

Inventors: Yuri Mirgorodski, et al.

Application No.: 10/664,469

Filed: September 17, 2003

For: METHOD OF PMOS STACKED-GATE

MEMORY CELL PROGRAMMING ENHANCEMENT UTILIZING STAIR-LIKE PULSES OF CONTROL GATE VOLTAGE

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Group Art Unit: 2818

Examiner: Unknown

CHANGE OF ATTORNEY OR AGENT'S ADDRESS IN APPLICATION (37 CFR 1.8(a))

353 Sacramento Street, Suite 2200 San Francisco, CA 94111 (415) 772-4900

**CERTIFICATE OF MAILING** 

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on June 7, 2004.

STALLMAN & POLLOCK LLP

Dated: 06/07/04

By: Marsha A. Townsend

Sir:

Please send all correspondence for this application to:

STALLMAN & POLLOCK LLP

Attn: Michael J. Pollock

353 Sacramento Street, Suite 2200

San Francisco, CA 94111 Customer No.: 28584

Please direct all telephone calls to:

Michael J. Pollock

Telephone: (415) 772-4900 Facsimile: (415) 398-2890

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: (2-7-2004

Michael J. Pollock

Reg. No. 29,098

Attorneys for Applicant(s)

Atty Docket No.: NSC1-M3500 [P05681]